

Apple IIgs

System Fact Snee	PT .			
SIEW	POWER		PCRIS ADB	
Introduced: September 1986 Discontinued: December 1992 Gestalt ID: Form Factor: Apple IIgs Weight (lbs.): 8.72 Dimensions (inches): 4.6 H x 11.2 W x 1. Codename: Cortland, Phoenix, Oder Number: A0012LL/A KB Article #: 7889, 1937, 10192	Amps: BTU Per Hour: Voltage Range: Freq'y Range (Hz): Battery Type:	Max. Watts: 60 Amps: .5 BTU Per Hour: 205.2 Voltage Range: 107-132 Freq'y Range (Hz): 50-60 Battery Type: ☐ Soft Power ☐ Monitor Power Outlet		S: 1 D: VGA 7: none II: none S: none t: none C: None S: None H: none C: None C: None C: None Remote Control
Built-in Display: VRAM Speed: VRAM Need	ed: Video Configuration: 2-bit color at 640x200 4-bit color at 320x200	512 640 x384 x400	Maximum Color B 640 640 800 83: x480 x870 ² x600 x62-	-
¹ 1-bit = Black & White; 2-bit = 4 colors; 4-bit = 16 colors; 8-bit = 256 colors; 16-bit = Thousands; 24-bit = Millions ² The maximum color depth listed for 640x870 is 8-bit, reflecting the capabilities of the Apple 15" Portrait Display.				
LOCOBOARD		MEVORY		
Main Processor: Western Design Center 65SC816, 2.8 MHz PMMU: none FPU: none Data Path: 16-bit, 1 MHz L1 Cache: none L2 Cache: none Secondary Processor: none Slots: 7 proprietary		Memory on Logic Board: 256k Minimum RAM: Maximum RAM: 8 MB RAM Slots: expansio Minimum RAM Speed: RAM Sizes: Install in Groups of:		
S OFTWARE	Speech Recognition Supported		apported Macintosh System Software:	
Addressing Modes: Original SSW: Original Enabler:	ROM APPLETAIK Versi		UX 1.0	ProDOS
SUND	. D T	STORAGE		y Size:
Ruilt-in Microphone	Microphone Port Type: Sound In: Sound Out: mono, 8-bit		Supports Internal HD Size: Internal HD Interface: CD-ROM Original CD-ROM Speed:	
HSTORY				
The last member of the Apple II line, The Western Design Center 65C816 processo offered with 256k of RAM, expandable to	r running at either 2.8 or 1 Mhz. It			

The IIgs also offered 128k of ROM, expandable to 1 MB. The IIgs shipped with a Mac-like interface and a IIgs-specific OS, and introduced the Apple Desktop Bus (ADB) port. (It also ran most other Apple II software.) The IIgs was the first computer to include a Large Scale Integration

Note: n/a = information not available or not applicable

(LSI) chip, designed by Steve Wozniak, and called the IWM (Integrated Woz Machine).