

## Umax SuperMac S900/200

System Fact Sheet	_			
Introduced: August 1996 Discontinued: August 1998 Gestalt ID: Form Factor: SuperMac S900 Weight (lbs.): 28 Dimensions (inches): 17.25 H x 7 W x 17.5 D  Codename: Oder Number: KB Article #: Called the Pulsar in the UK	Max. Watts: Amps: BTU Per Hour: Voltage Range: 100-240 V Freq'y Range (Hz): 50-60 Hz Battery Type: 3.6V lithium Soft Power Monitor Power Outlet		Flopp, SCS GeoPort Connector Etherne Microphone Port Typ Printer Modem	o: DB-15 y: none I: DB-25 s: 2 t: 10Base-T
Built-in Display:    Maximum Color Bit-depth   At:				
Main Processor: 604e, 200 MHz PMMU: none FPU: none Data Path: ,50 MHz L1 Cache: 32K L2 Cache: 512K-1MB Secondary Processor: ASPD slot Slots: 6PCI  The J700 and S900 have a PCI bridge chip controlling all PCI slots except the first two, which use the normal Apple chip. As a result, only cards that are PCI 2.1 compliant can be installed in slots 3-6 (3-4 for		Memory on Logic Board: 16MB Minimum RAM: 16-64MB Maximum RAM: 1GB RAM Slots: 8 168-pin Minimum RAM Speed: 70 ns RAM Sizes: 32, 64, 128 MB Install in Groups of: 2		
Addressing Modes: 32-bit Original SSW: 7.5.3 Original Enabler:	Recognition Supported  ROM  ROM Versi  ROM S  AppleTalk Versi	A/L   ID:	rted Macintosh System S UX 1.0 NOS 1.11 UX 1.11 NOS 1.3 UX 2.0 NOS 2.0 UX 3.0.1 NOS 2.1 UX 4.1.4 LOS 7/7 UX 4.1.4.1 DOS 3.1 UX 4.1.5 SOS 1.3	ProDOS
Ruilt-in Microphone	ype: l In: stereo, 16-bit Out: stereo, 16-bit		Floppy	Speed: 8x with the SuperMac
The J710 was the last new model from Umax and only about 50 were ever produced. Combining the form factor of the compact C500 (below monitor in photo) with the power of 604e or G3, the J710 would have been great for power users on a budget. About 90% shipped with the 200 MHz 604e. The G3 models were for internal use only.				

## Although Apple certified the model for sale, their refusal to renew Umax's license to produce Maclones lead Umax to abandon the J710.

The J710 looks just like the C500, the computer beneath the monitor in this photo. It also uses the same ZIP (or PEZ) socket for processor